

1. Features

- Typical 1dB bandwidth of 8.96 MHz
- High attenuation
- Single Ended Operation
- Dual In-line Package (DIP)
- RoHS/RoHS2 (2015/863/EU) Compliant

RoHS Compliant

Tested by SGS Testing Korea

2. Electrical Specifications

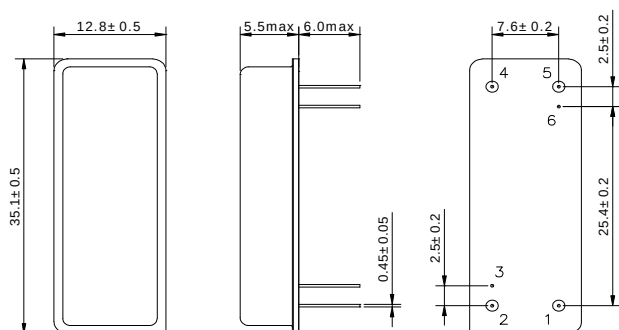
Source and Load Impedance = 50Ω

Room Temperature : +25 °C		Minimum	Typical	Maximum
Center Frequency (fo)	MHz	-	160.0	-
Insertion Loss	dB	-	25.0	27.0
1dB Bandwidth	MHz	8.7	8.96	-
3dB Bandwidth	MHz	-	9.17	-
40dB Bandwidth	MHz	-	10.05	10.2
Amplitude Ripple (fo ± 4.3 MHz)	dB	-	0.5	1.0
Group Delay Variation (fo ± 4.3 MHz)	nsec	-	75	200
Absolute Delay	usec	-	4.37	-
Ultimate Rejection	dB	45	50	-
Temperature Coefficient of Frequency	ppm/°C	-	-18	-

* Input POWER : +10dBm

Due to variances in a PCB layout and parasitics, actual matching values may have to be changed accordingly.

D3512 Package Dimension



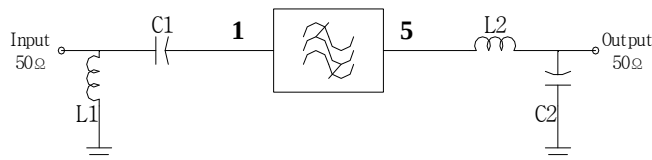
Dimensions shown are nominal in millimeters

Base : Fe(SGCC), Au plating over Ni plated

Cap : Cu & Cr Alloy, Ni Plated

Termination : Kovar, Au Plated

Matching Schematic



L1 = 18nH, C1 = 110pF, L2 = 18nH, C2 = 56pF

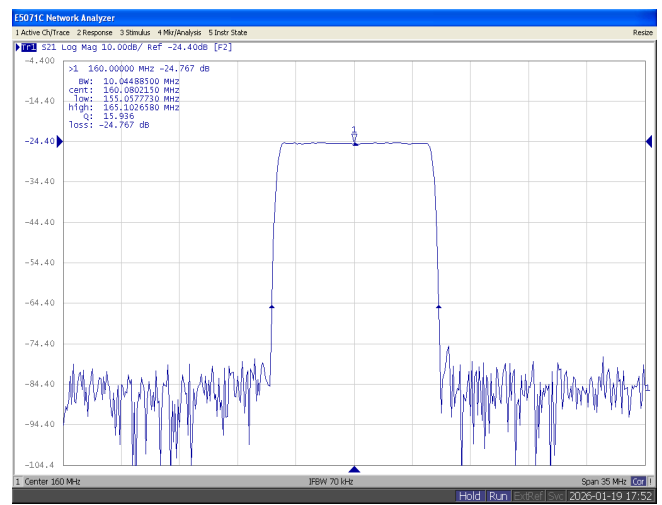
Pin Configuration			
Input	1	Others	Ground
Output	5		

3. Typical Performance (at +25°C)

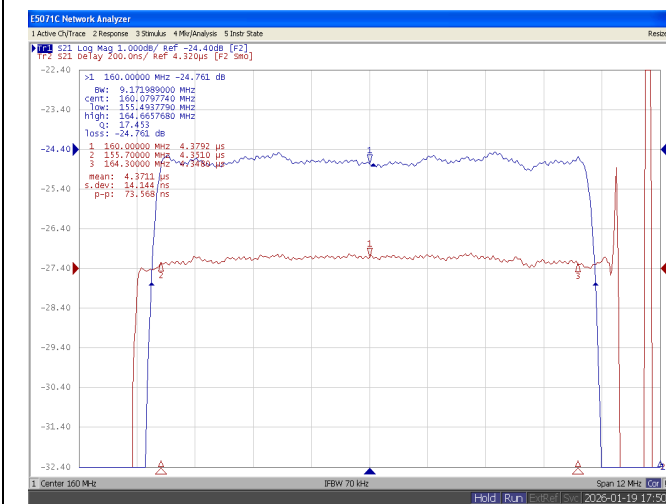
1dB Bandwidth & Ripple



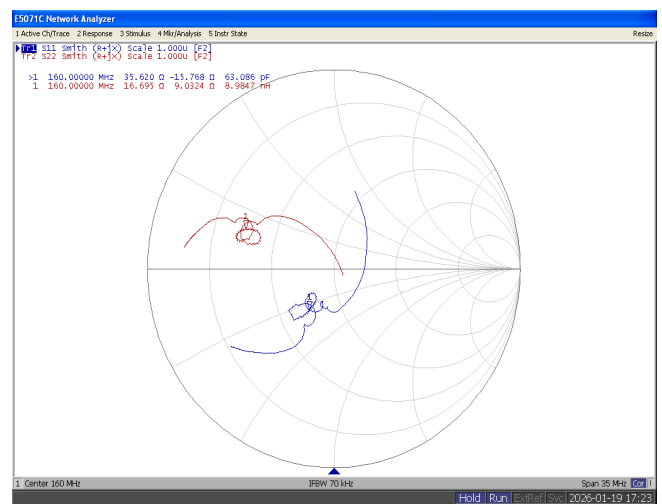
40dB Bandwidth



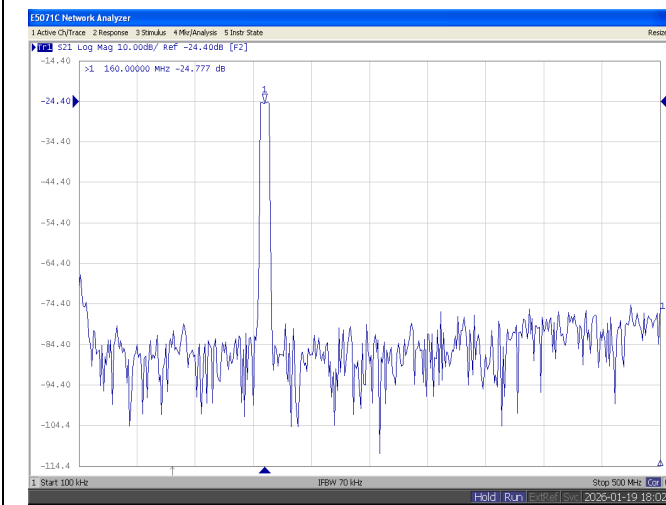
3dB Bandwidth & Group Delay



Smith Chart (S11 & S22)



Wide band Properties



SWR (S11 & S22)

